

HS-1000S ETM TRACE PORT ANALYZER

THE HS-1000S REPRESENTS ONE OF AMERICAN ARIUM'S MOST ADVANCED TRACE PORT ANALYZERS FOR TARGETS WITH ARM®-ARCHITECTURE CORES. THIS EMULATOR OPERATES ON MICROSOFT® WINDOWS® AND LINUX PLATFORMS AND SUPPORTS ARM ETM (EMBEDDED TRACE MACROCELL) REAL TIME TRACE AND DELIVERS 256 MBYTES OF TRACE MEMORY AT A BLAZING 680 MHZ CAPTURE RATE. FEATURING SERIAL WIRE DEBUG FOR ARM CORES INCORPORATING CORESIGHT™ TECHNOLOGY, THE HS-1000S OFFERS RELIABLE FUNCTIONALITY OF AND SUPERIOR VISIBILITY TO YOUR EMBEDDED SOURCE CODE.

DESIGNED FOR ARM, XSCALE, AND TI-OMAP™ CORE ARCHITECTURES:

- Supports full instruction set for ARM7™, ARM9™, ARM11™, and Cortex™ cores (along with CoreSight)

- Supports full instruction set for all XScale processors, including I/O, application, network, and control plane processors
- Supports TI-OMAP processors

ARM DEBUG SUPPORT

- Non-intrusive debug
- Real time operation
- Heterogeneous processor support
- Support for different ETM trace configurations on a single processor
- Serial wire debug
- XScale trace support
- RealMonitor™ support
- High speed downloads
- Independent processor control
- Reliable breakpoint control
- Semihosting support
- PLD programming
- Flash programming
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TOOLCHAINS SUPPORTED:

- ARM Development Suite 1.2
- ARM RealView® Developer Suite
- Green Hills® C/C++
- GNU C/C++
- IAR Embedded Workbench®

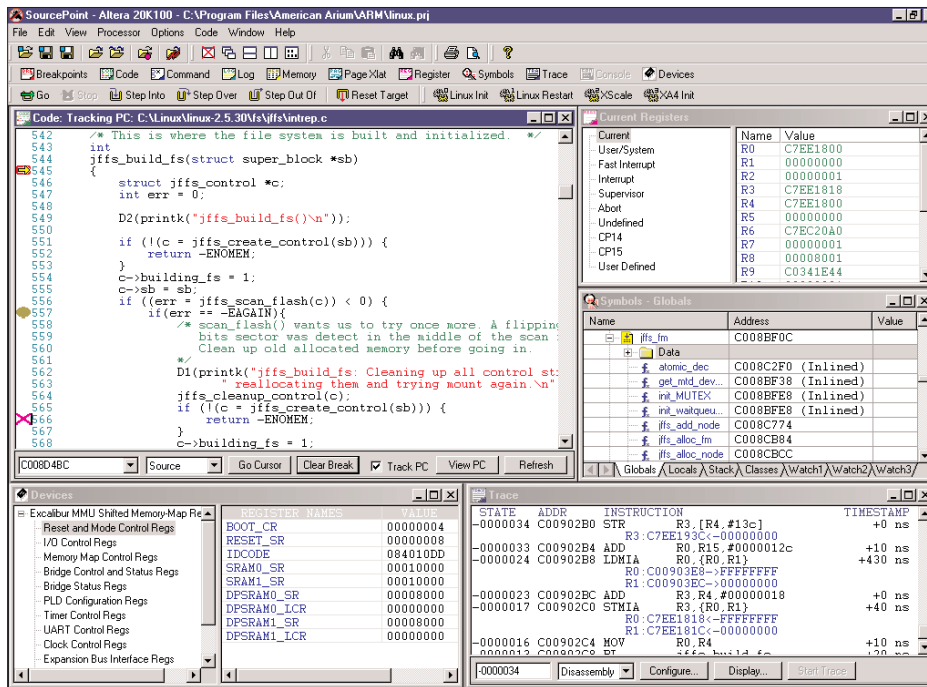


KEY BENEFITS

- **FASTEST TRACE CAPTURE RATE IN THE INDUSTRY** - The HS-1000S offers real-time trace with a 680 MHz acquisition rate for faster detection of software and firmware bugs.
- **INTEGRATED & INTUITIVE** - The HS-1000S, with its companion software debugger, delivers a highly integrated, intuitive environment designed to help you minimize iterative steps and shorten your debug cycle.
- **USER-FRIENDLY** - The HS-1000S is easy to install and configure. Downloads are fast. And the debugging solution is reliable.
- **FLEXIBLE** - The HS-1000S works with multiple ARM-architecture cores on both Microsoft Windows and Linux hosts. Unlike some debuggers, you don't need a different emulator for each processor type.
- **FULLY SUPPORTED** - All Arium units produced for the embedded market are fully supported by our highly trained staff of applications engineers.

Processor	Core Clock Rate	Trace Size (256 MBytes)	
		No Timestamp	With Timestamp
Coretex R4/M3	340 MHz full-rate 680 MHz half-rate	8-Bit Packets/Record 256 million packets	8-Bit Packets/Record 128 million packets
ARM11	340 MHz full-rate 680 MHz half-rate	8-Bit Packets/Record 256 million packets	8-Bit Packets/Record 128 million packets
ARM9	340 MHz full-rate 680 MHz half-rate	8/4-Bit Packet 160 million records	8/4-Bit Packet 80 million records
		16-Bit Packet 96 million records	16-Bit Packet 64 million records

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SOURCEPOINT™ INTERFACE

Included with the HS-1000S is American Arium's SourcePoint debugging software. The debugger interface is part of the company's core technology, developed specifically for SoC design and debug. The 32-bit application runs on Microsoft Windows 2000/XP/Vista and numerous Linux platforms and supports targets running various operating systems, including Linux.

Key features include:

- Support of all popular compilers
- Integrated, real time trace
- Innovative, seamless approach to Linux kernel, device driver, and applications debug
- Multi-core and heterogeneous debug and independent processor control
- Robust, user-friendly C-like command language
- Serial wire debug

SPECIFICATIONS

Environmental

32-90° F (0-31° C)
Maximum humidity - 85%

Communications

10/100Base-T Ethernet
USB 1.1

Download Speed

>200 KBytes/sec

BNC Trig In

>50 Ohm, 155 ns maximum delay

BNC Trig Out

50 Ohm, 125 ns maximum delay

JTAG Rate

1 KHz - 40 MHz

JTAG Logic Levels

1.2 - 3.3 nominal

Dimensions (in/cm)

(W) 5.25/13.34
(L) 5.5/13.97
(H) 1.63/4.14

PC HOST REQUIREMENTS

- PC host (Intel Pentium® III or better or equivalent)
- SVGA monitor (1024 x 768 or higher)
- 60 MB hard disk space, 128 MB RAM
- 10/100Base-T Ethernet or USB 1.1

VALIDATED OPERATING SYSTEMS

- Microsoft Windows 2000/XP/Vista
- Linux Red Hat 8.0/9.0
- Linux Enterprise WS 4.0
- Linux Fedora Core 6
- Linux Mandriva Spring 2007
- Linux Suse 10.2

ORDERING INFORMATION

STANDARD

Includes the base unit, JTAG, SWD and trace cables, SourcePoint debugger (Microsoft Windows), power supply, documentation, and one year service agreement.

HS-1000S JTAG/SWD/ETM trace emulator

OPTIONS:

- SP-LNX** SourcePoint for Linux hosts
- DPA-2014** 20-14 pin JTAG adapter*
- HSS-MRV** Serial trace adapter
- INT-INNV** OMAP Innovator Board trace interposer
- STAR1-HSE** Extended service agreement

THE COMPANY

American Arium has been a major supplier of emulator debugging tools for more than 20 years, servicing both the embedded and PC markets. Arium hardware-assisted solutions support ARM®-based processor cores, XScale, and TI OMAP™ processors, as well as AMD and Intel laptop, desktop, and server processors (excluding Intel® Itanium® processors).

American Arium's mission is to meet and exceed the needs of firmware and software developers by providing them with the best tools to help them minimize their development/debug cycle.

It's Hard to Compete Without the Right Tools

